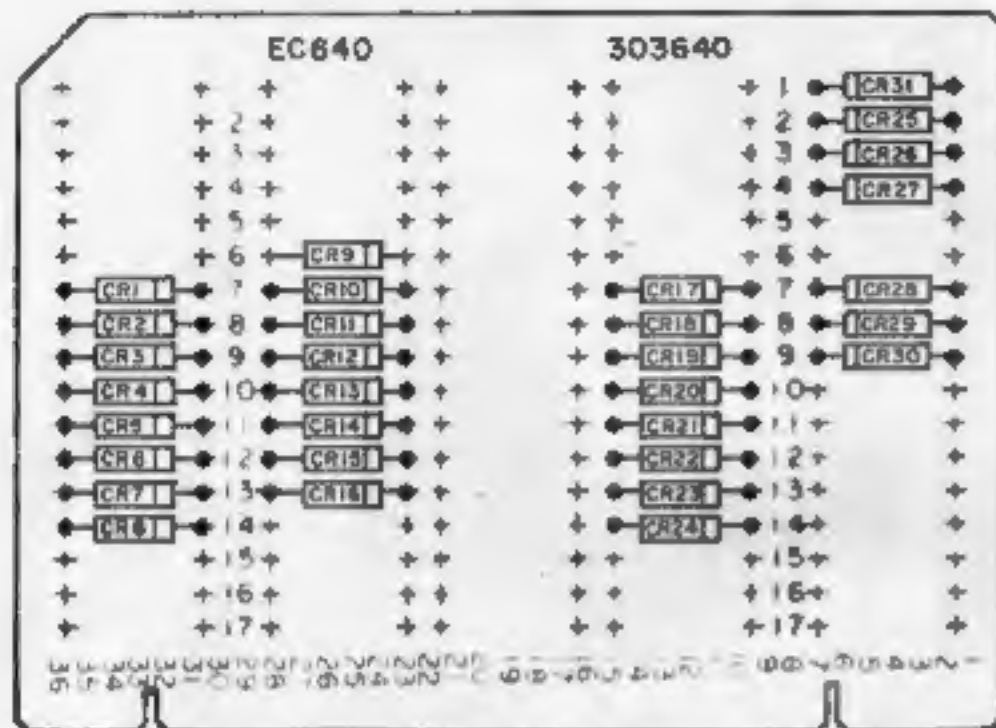


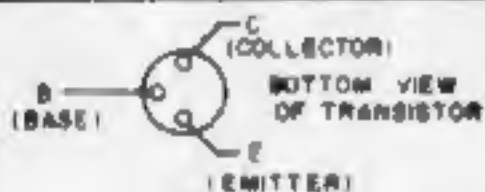
EC 638
303638

CIRCUIT BOARD EC638

REVISIONS		
ISSUE	DATE	AUTH. NO.

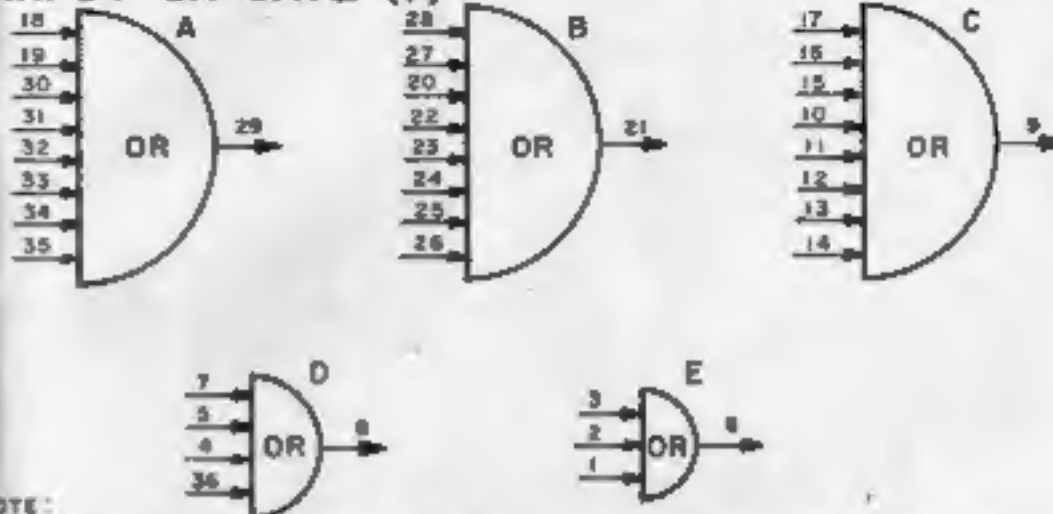


NOTE
REFER TO 6050 WD FOR BASIC MARKING INFORMATION

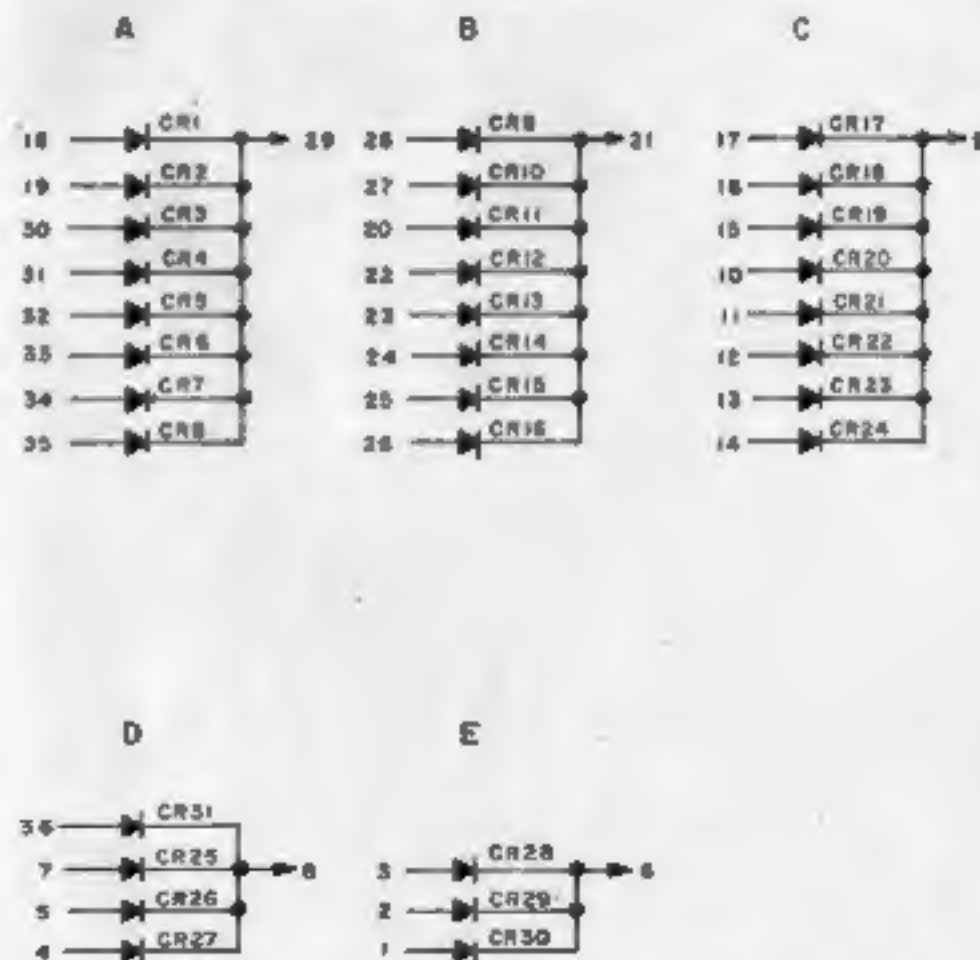


FIVE "OR" GATES ARE PROVIDED. THREE HAVE 8 INPUTS, ONE HAS 3 INPUTS AND ONE HAS 4 INPUTS. ALL GATES FUNCTION IDENTICALLY, A ZERO VOLT LEVEL ON ANY INPUT RESULTS IN A ZERO VOLT OUTPUT FROM THE CORRESPONDING GATE. WHEN ALL INPUTS TO A PARTICULAR GATE ARE AT -6 VOLTS, THE CORRESPONDING OUTPUT IS -6 VOLTS. EXTERNAL BIAS RESISTOR TO -12V. IS REQUIRED.

8 INPUT OR GATE (3)
3 INPUT OR GATE (1)
4 INPUT OR GATE (1)



NOTE:
WHEN TWO OR MORE GATES ARE TIED TOGETHER ONE SYMBOL CAN BE USED SHOWING PROPER TERMINAL DESIGNATIONS.



ALL DIODES ARE 02

